

FIG 1

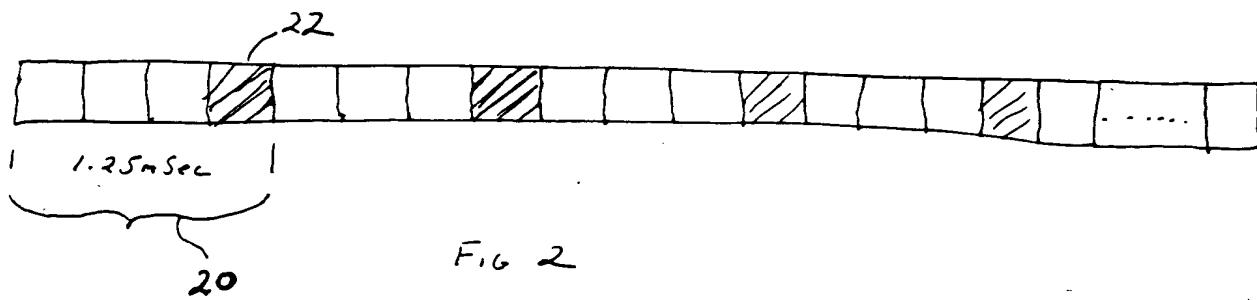


FIG 2

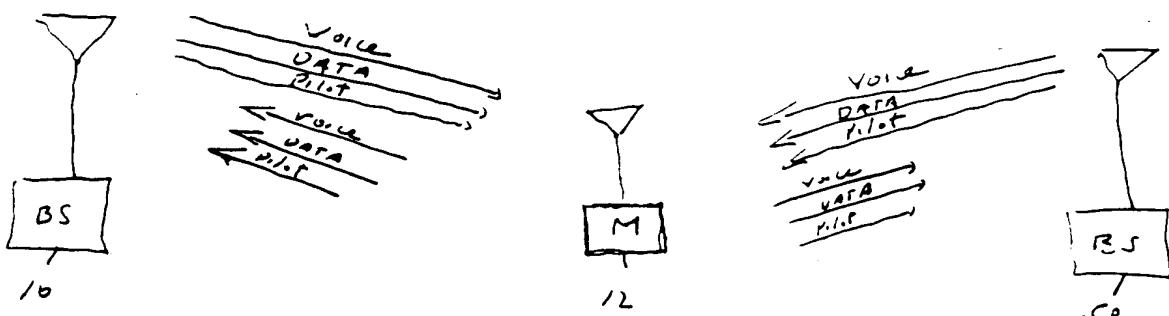


FIG 3

00000000000000000000000000000000

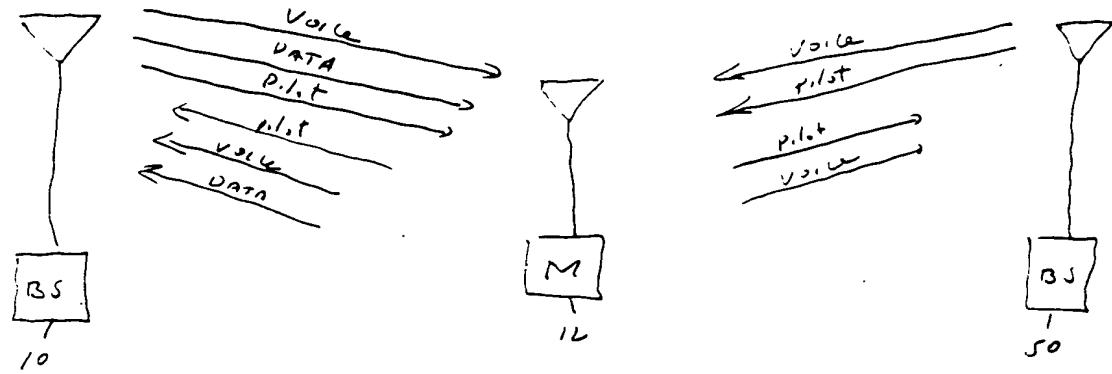


FIG 4

00000000000000000000000000000000

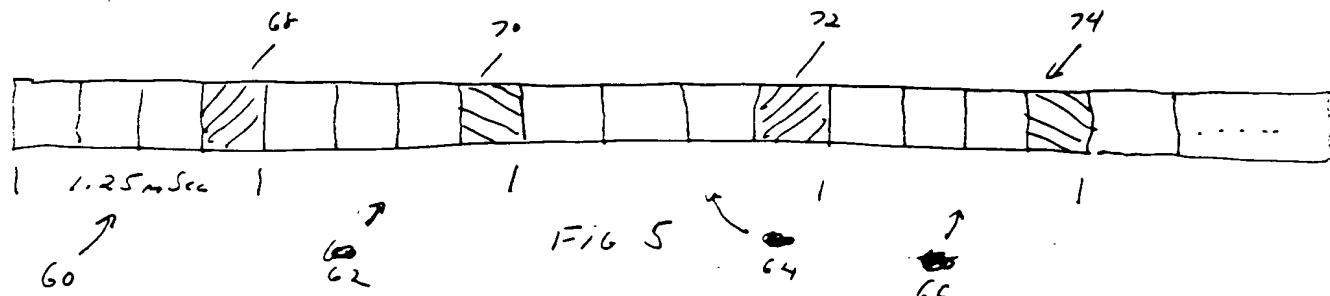


FIG 5

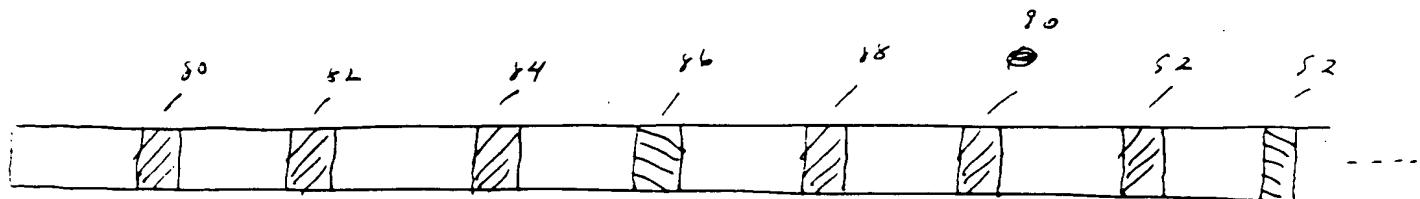


FIG 6

CONFIGURATION NAMEPower Control Bit Pattern (Example)

V50D750

V DDD DDDD DDDD DDDD

V100D700

V DDD DDDD VDDD DDDD

V150D650

V200D600

VDDD VDDD VDDD VDDD

V250D550

V300D500

VDVD VDDD VDVD VDDD

V350D450

V400D400

VDVD VDVD VDVD VDVD

V750D50

VDVV VDVV VDVV VDVV

FIG 7

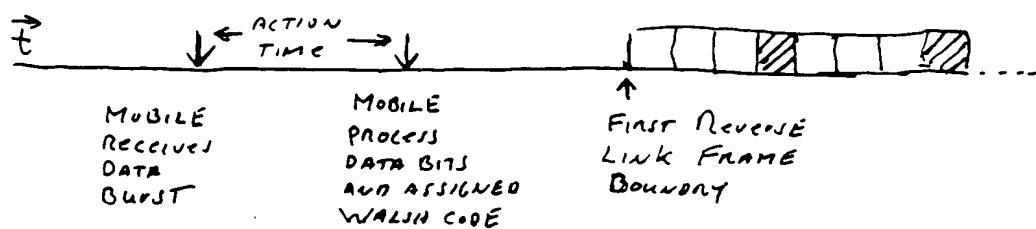


FIG. 8

0020020000000000